



Docket No.: WMP-SME-352

#8 ELECTION 2811  
5/29/03  
Mallari

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By: Markus Nollf

Date: May 21, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : Klemens Ferstl et al.  
Applic. No. : 10/022,605  
Filed : December 17, 2001  
Title : Multichip Module for LOC Mounting and Method for Producing the Multichip Module  
Examiner : Cuong Qunag Nguyen - Art Unit: 2811

RESPONSE

Hon. Commissioner of Patents and Trademarks

Sir:

Responsive to the Office Action dated May 2, 2003, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-15, for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claim 1-15 is solicited.

Respectfully submitted,

Markus Nollf  
For Applicants

Markus Nollf  
Reg. No. 37,006

Date: May 21, 2003

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